



Electronic Tuning-Use FM Front End for Car Radio, Home Stereos

Overview

 The LA1177 is an FM front end IC for use in car radio, home stereo applications. It requires fewer external parts.
The on-chip oscillator and oscillation buffer facilitate designing of electronic tuning sets.

Features

- Wide-band AGC circuit (Improvement in intermodulation, cross modulation characteristics).
- On-chip local oscillation buffer (For electronic tuning).

Functions

- Oscillator, oscillation buffer.
- Mixer.
- Wide-band AGC circuit.
- IF amplifier.

Specifications

Maximum Ratings at Ta=25°C

	5				
	Parameter	Symbol	Conditions	Ratings	Unit
	Maximum supply voltage	V _{CC} max	Pins 2, 9	10	V
	Allowable power dissipation	Pd max	Ta≤70°C	440	mW
	Operating temperature	Topr		-20 to +70	°C
ĺ	Storage temperature	Tstg		-40 to +125	°C

Operating Conditions at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	Vcc		8	V
Operating voltage range	V _{CC} op		8 to 9	V

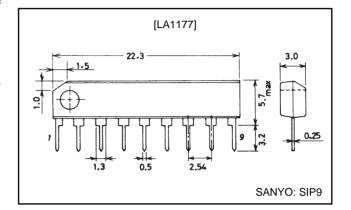
Electrical Characteristics at Ta=25°C, V_{CC}=8V, fin=88MHz

Parameter	Symbol	Conditions -	Ratings			Unit
1 drameter	Cymbol		min	typ	max	Onne
Quiescent current	Icco	No input	21	26	31	mA
AGC high-level voltage	V _{AGC} -H	V _{IN} =0dBµ	7.7	8.0		V
AGC low-level voltage	V _{AGC} -L	V _{IN} =100dBµ		0.07	0.3	V
AGC mixer input voltage	V _{i AGC}	V _{AGC} ≤2V, Pin 3	73	80	87	dΒμ
IF saturation output voltage	V _{IF} -max	V _{IN} =1.0dBµ	108	112	116	dΒμ
Input limiting voltage	V _{i lim}		76	83	90	dΒμ
Voltage gain	VG	V _{IN} =65dBµ	88	92	96	dΒμ
Local OSC output voltage	Vosc	No input, 75Ω termination	80	84	88	dΒμ

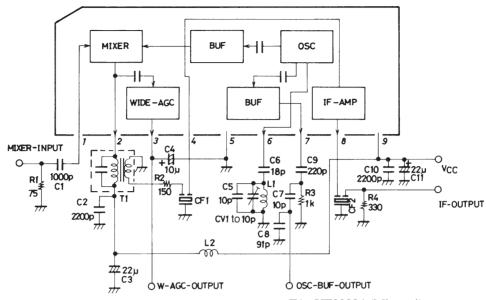
Package Dimensions

unit: mm

3017C-SIP9



Evaluation Circuit and Internal Equivalent Circuit Block Diagram



Unit (resistance : Ω , capacitance : F)

T1 : YT30224 (Mitsumi) L1 : HU-50448 (Mitsumi)

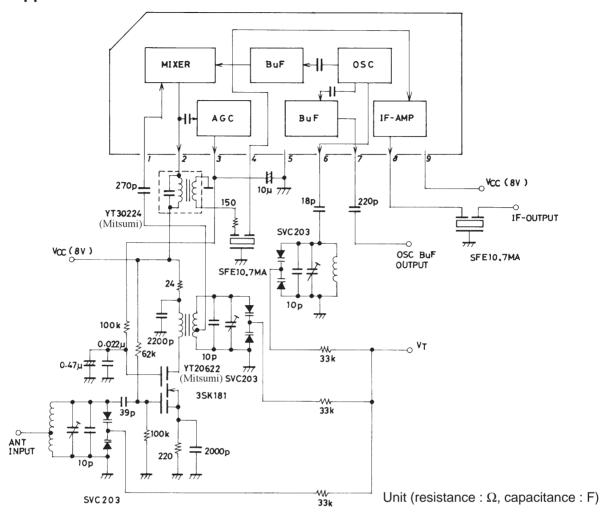
CF1-CF2: SFE10.7MA (Murata)

Typical Voltage on Each Pin

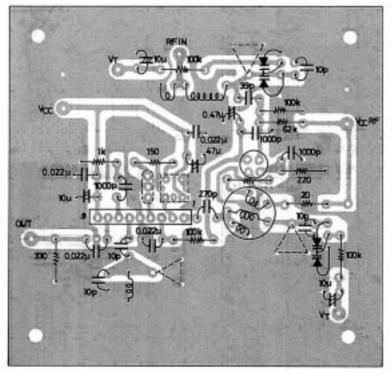
Pin No.	Typical voltage	Description	Remarks
1	2.7V	Mixer input	
2	8.0V	Mixer output	
3	8.0V	AGC input	No input
4	2.0V	IF input	
5	0V	GND	
6	4.9V	Oscillator base terminal	
7	1.4V	Oscillation buffer output	
8	4.4V	IF output	
9	8.0V	Vcc	

Note: Extreme caution should be exercised when applying voltage across pin 9 (+) and other pins as dielectric breakdown may occur.

Sample Application Circuit

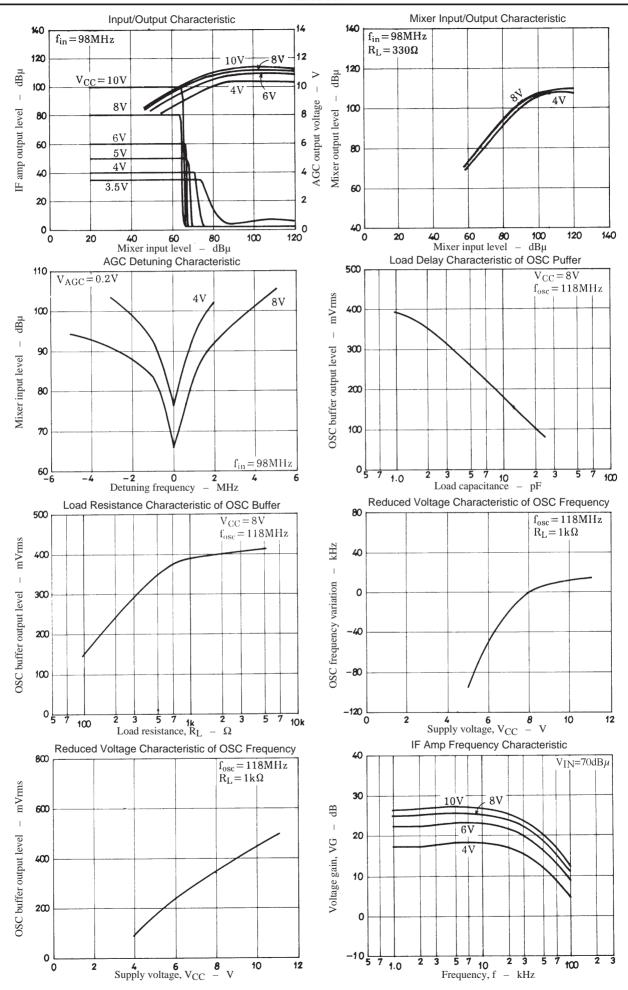


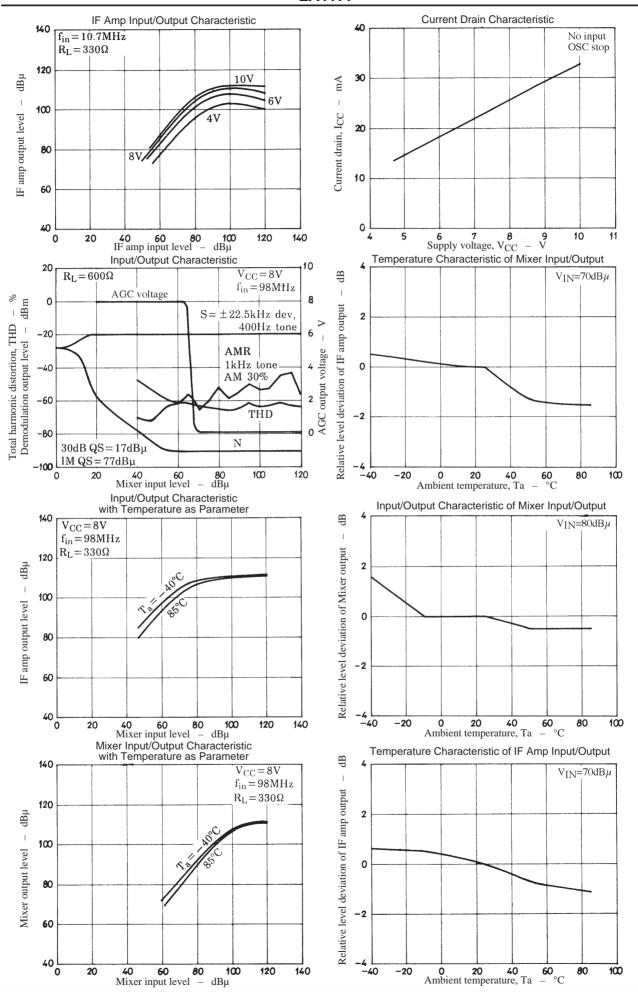
Sample Printed Circuit Pattern

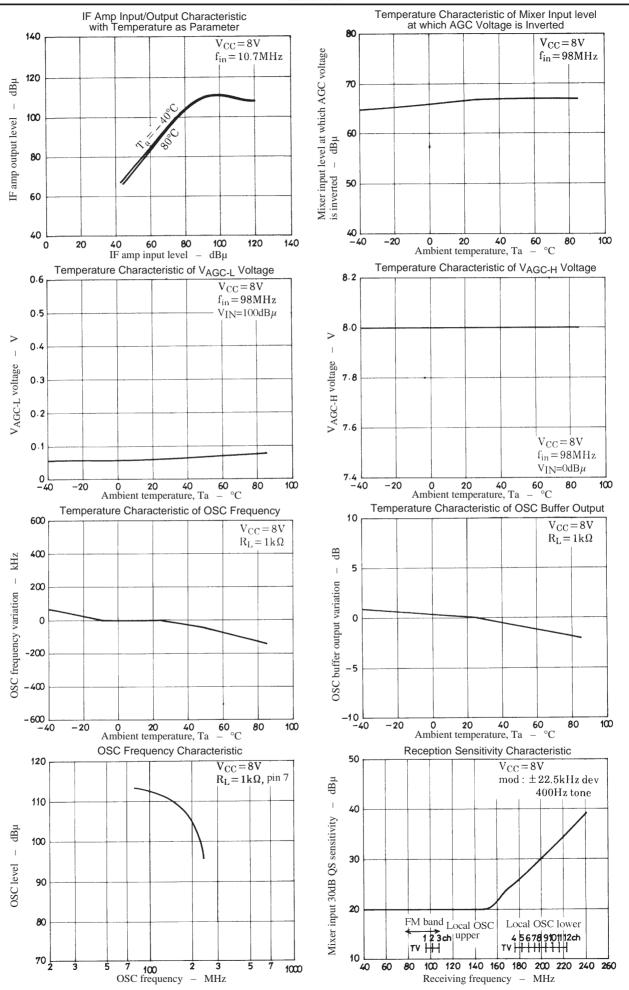


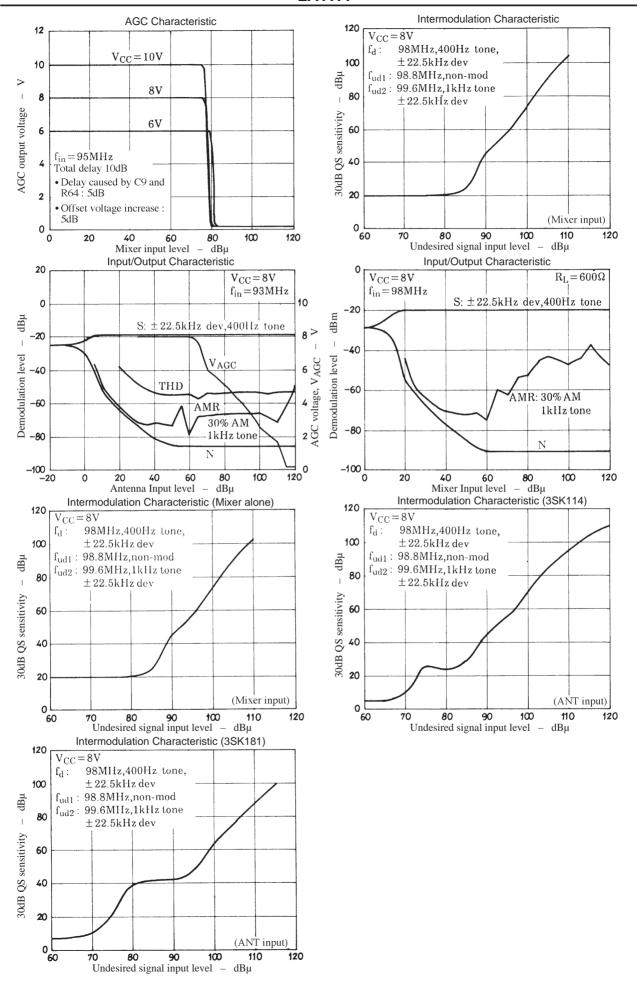
Cu-foiled area 85×82 mm²

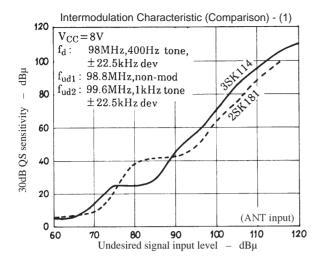
Unit (resistance : Ω , capacitance : F)

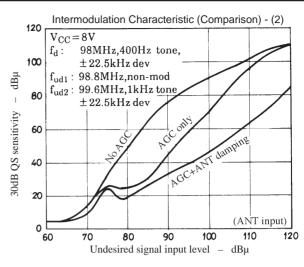












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